


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

[Advanced Scholar Search](#)
[Scholar Preferences](#)
[Scholar Help](#)

Scholar All articles - **Recent articles** Results **1 - 10** of about **6,570** for **context switching <near> thread interleaving**. (0.38 seconds)

APRIL: A Processor Architecture for Multiprocessing - *ncstrl.org (TXT)

A Agarwal, BH Lim, D Kranz, J Kubiatowicz - portal.acm.org

... not support cycle-by-cycle **interleaving of threads** ... ecutes instructions from a given **thread** until it ... a sim- ple processor design with **context switch** overheads of ...

[Cited by 388](#) - [Related articles](#) - [Web Search](#) - [All 14 versions](#)

Interleaving: a multithreading technique targeting multiprocessors and workstations

J Laudon, A Gupta, M Horowitz - ACM SIGPLAN Notices, 1994 - portal.acm.org

... the need for large numbers of active contexts and the poor single-**thread** per- formance, are not intrinsic limitations of cycle-by-cycle **context switching**. ...

[Cited by 190](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 8 versions](#)

An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads - *kilac.jp (PDF)

H Hirata, K Kimura, S Nagamine, Y Mochizuki, A ... - portal.acm.org

... ters an absence of data, the processor rapidly **switches** ... fetches at most B instruc- tions for one **thread** every C ... in an **interleaved** fashion for multiple **threads**. ...

[Cited by 174](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 6 versions](#)

Multithreaded processor architectures

GT Byrd, MA Holliday - Spectrum, IEEE, 1995 - ieeeexplore.ieee.org

... instructions from the same **thread** and **switching** ... the hardware; the cost of **switching** be- tween ... of cycles typically executed between **context switches**, called the ...

[Cited by 68](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 2 versions](#)

Hybrid technology multithreaded architecture

G Gao, KK Likharev, PC Messina, TL Sterling - Frontiers of Massively Parallel Computing, 1996. Proceedings ..., 1996 - ieeeexplore.ieee.org

... of realistically achieving **near**-petaflops per ... overheads for process **switching**, synchronization and ... **Interleaved thread** execution requires a fundamental change ...

[Cited by 36](#) - [Related articles](#) - [Web Search](#) - [All 7 versions](#)

Replay for concurrent non-deterministic shared-memory applications

M Russinovich, B Cogswell - Proceedings of the ACM SIGPLAN 1996 conference on ..., 1996 - portal.acm.org

... by different pro- cesses or **threads** may be ... application the coarse-grained **interleaving** characteristics can be ... schedule, ensuring that **context switches** occur at ...

[Cited by 49](#) - [Related articles](#) - [Web Search](#) - [Library Search](#) - [BL Direct](#) - [All 3 versions](#)

Dataflow architectures and multithreading

B Lee, AP Hurson - Computer, 1994 - ieeeexplore.ieee.org

... **Interleaving** can be done in many ways, that is, on every cycle, on remote reads, and so ... **Context switching** can occur on every cycle or when a **thread** dies due ...

[Cited by 56](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 7 versions](#)

MSparc: A Multithreaded Sparc

A Mikschl, W Datum - LECTURE NOTES IN COMPUTER SCIENCE, 1996 - Springer

... latency operations by **switching** from one **context** to another. one design issue of multithreaded architectures is whether **interleaving of thread** execution is ...

[Cited by 27](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 6 versions](#)

Multi-threaded microprocessor architecture utilizing static interleaving

JC Chung, CL Wu - US Patent 5,404,469, 1995 - freepatentsonline.com

... and gate counts expected in the **near** future for ... Because no **context-switch** overhead

exists among **threads** which ... **interleaving** unit selects a **thread** according to a ...

[Cited by 52](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

Context-switching techniques for decoupled multithreaded processors

J Kreuzinger, T Ungerer - Euromicro'99, 1999 - doi.ieeecs.org

... poor. The block **interleaving** approach executes a single **thread** until it reaches a situation that triggers a **context switch**. Usually ...

[Cited by 11](#) - [Related articles](#) - [Web Search](#) - [All 4 versions](#)

Key authors: [A Agarwal](#) - [B Lim](#) - [D Kranz](#) - [J Kubiawicz](#) - [D Chaiken](#)

Goooooooooooooogle ►

Result Page: 1 2 3 4 5 6 7 8 9 10 [Next](#)

context switching <near> thread inte

Search

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2008 Google